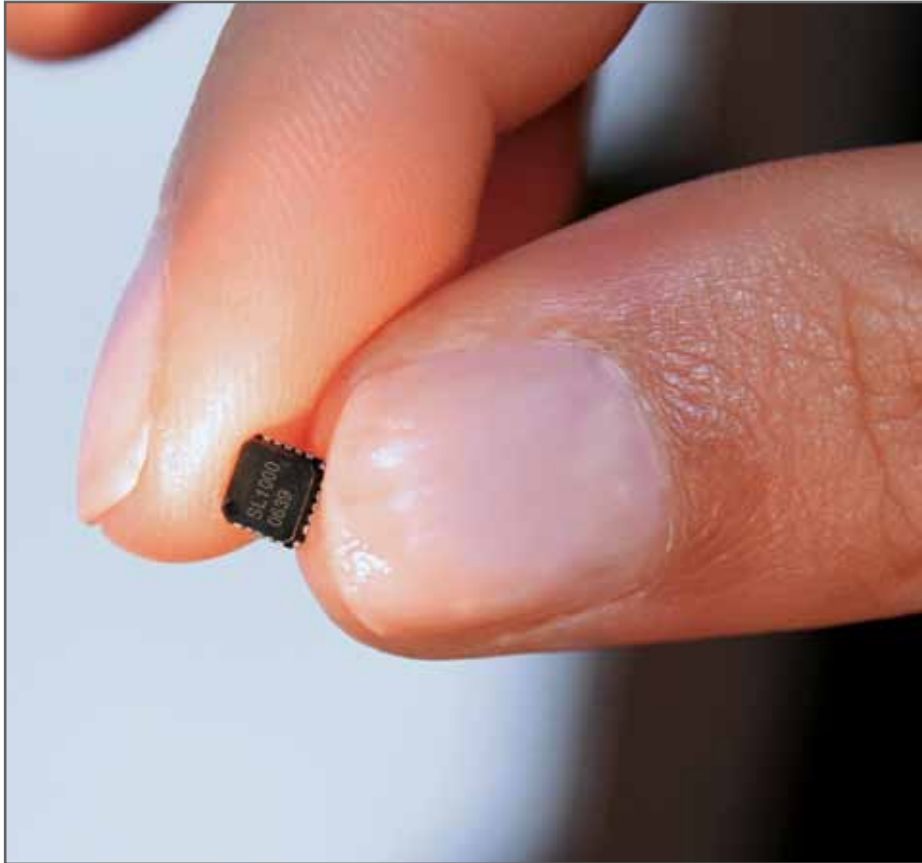


ASIC



What benefits do our ASIC-competence and technology offer the customer?

Benefits with ASIC

Quick time-to-market

Ready-made building blocks make it possible to reduce both development costs and development time. In other words, you can have your product on the market faster and at a lower cost thanks to our building block system.

The SL1000 ultra low power RF transceiver is part of ShortLink's ASIC building blocks portfolio focused on low power for RF and mixed-signal applications such as audio and precision amplifiers.

AD & DA converters and **low-noise amplifiers** are other examples of advanced blocks in the portfolio which reduce the ASIC design time and enable a quicker time-to-market.

ShortLink offers a **System-on-Chip design** optimized with exactly the functionality and performance that is needed in each application.

This saves power, space and cost!

Copy protection

In practise, it is almost impossible to copy a mixed-signal ASIC design.

Low production costs

The cost for an ASIC is often several times lower than an equal discrete solution. Furthermore, the amount of components in the product is significantly reduced.

Reliability

Minimizing the total number of design components leads to very high reliability, that is, low error frequency. Also, an ASIC is rigorously tested before delivery. This all results in considerably lower maintenance costs.

Unique performance

With our knowledge, we can often achieve a mixed-signal ASIC design, with unique performance at a very low power consumption, which is not possible with a discrete design.

Ultra low power

It is often possible for a battery-driven product to achieve an operating time which lasts several years on just a small coin cell battery.

Small form factor

A System-on-Chip solution can drastically reduce the size of a product. For example, a complete hearing aid product can be fitted inside the ear.

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